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for FY 2003				Inven	tor	Graham k		
Effective 01/01/2003, Patent fees are subject to annual revision.			ner Na			W. Choi		
Applicant claims small entity status. See 37 CFR 1.27	Art Unit					2186 M4065.0340/P34 <b>RECE</b>		
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Name Oshinsky LLP The Director is authorized to: (check all that apply)	1052	50	2052	25	Surcharge sheet.	<ul> <li>late provisio</li> </ul>	nal filing fee or cov	er
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FEE CALCULATION		110	2251	55	Examiner	action for reply within		
1. BASIC FILING FEE		410	2252				second month	
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Fee Fee Fee Fee Fee Description Fee Paid Code (\$) Code (\$)	1254	1,450	2254	725	Extension	for reply within	fourth month	
1001 750 2001 375 Utility filing fee	1255	1,970	2255	985	Extension	for reply within	fifth month	
1002 330 2002 165 Design filing fee		320	2401		Notice of A		_	
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AMEN		Docket No. M4065.0340/P340						
Application No. Filing 09/652,003-Conf. #2935 August 3					Examiner	Art Unit 2186		
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Thomas V. D'Ai Attorney Reg. I								
DICKSTEIN SH 2101 L Street N Washington, Do (202) 828-2232	NW C 20037-1526		Y LLP				``	



Docket No.: M4065.0340/P340/PATENT)

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Graham Kirsch

Confirmation No.: 2935

Application No.: 09/652,003

Art Unit: 2186

Filed: August 31, 2000

Examiner: W. Choi

For: METHOD AND APPARATUS FOR

CONNECTING A MASSIVELY PARALLEL

PROCESSOR ARRAY TO A MEMORY ARRAY IN A BIT SERIAL MANNER

OCT 0 8 2003
Technology Center 2100

## AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

## **INTRODUCTORY COMMENTS**

In response to the Office Action dated July 1, 2003 (Paper No. 12), finally rejecting claims 1-17, 22-41, 43-49 and 51, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 29 of this paper.

10/03/2003 EFLORES 00000025 09652003

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